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(54) OHMIC CONTACT TO SEMICONDUCTOR

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Field of Classification Search

CPC H01L 33/06; H01L 33/0045 USPC 257/103, 79, 190 See application file for complete search history.

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(57)ABSTRACT

A solution for forming an ohmic contact to a semiconductor layer is provided. A masking material is applied to a set of contact regions on the surface of the semiconductor layer. Subsequently, one or more layers of a device heterostructure are formed on the non-masked region(s) of the semiconductor layer. The ohmic contact can be formed after the one or more layers of the device heterostructure are formed. The ohmic contact formation can be performed at a processing temperature lower than a temperature range within which a quality of a material forming any semiconductor layer in the device heterostructure is damaged.

20 Claims, 7 Drawing Sheets

